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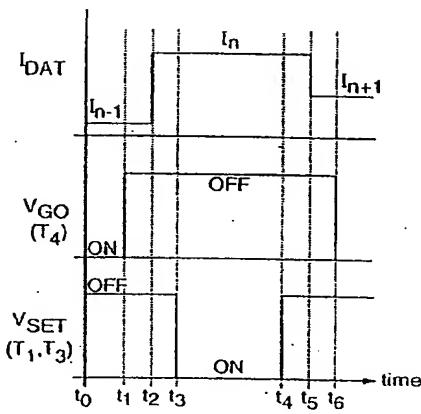
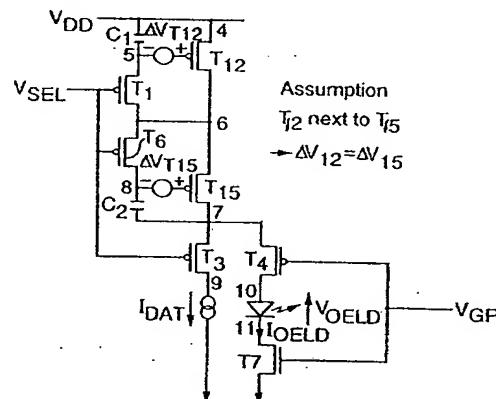
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(54) Current driven electrooptical device, e.g. organic electroluminescent display, with complementary driving transistors to counteract threshold voltage variations

(57) A driver circuit comprises a p-channel transistor and an n-channel transistor connected as a complementary pair of transistors to provide analog control of the drive current for a current driven element, preferably an organic electroluminescent element (OEL element). The transistors, being of opposite channel, compensate for any variation in threshold voltage  $\Delta V_T$  and therefore provide a drive current to the OEL element which is relatively independent of  $\Delta V_T$ . The complementary pair of transistors can be applied to either voltage driving or current driving pixel driver circuits.

Fig. 7.



**Description**

[0001] The present invention relates to a driver circuit. One particular application of such a driver circuit is for driving an organic electroluminescent element.

[0002] An organic electroluminescent (OEL) element OEL element comprises a light emitting material layer sandwiched between an anode layer and a cathode layer. Electrically, this element operates like a diode. Optically, it emits light when forward biased and the intensity of the emission increases with the forward bias current. It is possible to construct a display panel with a matrix of OEL elements fabricated on a transparent substrate and with at least one of the electrode layers being transparent. It is also possible to integrate the driving circuit on the same panel by using low temperature polysilicon thin film transistor (TFT) technology.

[0003] In a basic analog driving scheme for an active matrix OEL display, a minimum of two transistors are required per pixel. Such a driving scheme is illustrated in Figure 1. Transistor T<sub>1</sub> is provided to address the pixel and transistor T<sub>2</sub> is provided to convert a data voltage signal V<sub>Data</sub> into current which drives the OEL element at a designated brightness. The data signal is stored by a storage capacitor C<sub>storage</sub> when the pixel is not addressed. Although p-channel TFTs are shown in the figure, the same principle can also be applied for a circuit utilising n-channel TFTs.

[0004] There are problems associated with TFT analog circuits and OEL elements do not act like perfect diodes. The light emitting material does, however, have relatively uniform characteristics. Due to the nature of the TFT fabrication technique, spatial variation of the TFT characteristics exists over the extent of the display panel. One of the most important considerations in a TFT analog circuit is the variation of threshold voltage,  $\Delta V_T$ , from device to device. The effect of such variation in an OEL display, exacerbated by the non perfect diode behaviour, is the non-uniform pixel brightness over the display area of the panel, which seriously affects the image quality. Therefore, a built-in circuit for compensating a dispersion of transistor characteristics is required.

[0005] A circuit shown in figure 2 is proposed as one of built-in for compensating a variation of transistor characteristics. In this circuit, transistor T<sub>1</sub> is provided for addressing the pixel. Transistor T<sub>2</sub> operates as an analog current control to provide the driving current to the OEL element. Transistor T<sub>3</sub> connects between the drain and gate of transistor T<sub>2</sub> and toggles transistor T<sub>2</sub> to act either as a diode or in a saturation mode. Transistor T<sub>4</sub> acts as a switch in response to an applied waveform V<sub>GP</sub>. Either Transistor T<sub>1</sub> or transistor T<sub>4</sub> can be ON at any one time. Initially, at time t<sub>0</sub> shown in the timing diagram of Figure 2, transistors T<sub>1</sub> and T<sub>3</sub> are OFF, and transistor T<sub>4</sub> is ON. When transistor T<sub>4</sub> is OFF, transistors T<sub>1</sub> and T<sub>3</sub> are ON, and a current I<sub>DAT</sub> of known value is allowed to flow into the OEL element, through transistor T<sub>2</sub>. This is the programming stage because the

threshold voltage of transistor T<sub>2</sub> is measured with transistor T<sub>3</sub> turned ON which shorts the drain and gate of transistor T<sub>2</sub>. Hence transistor T<sub>2</sub> operates as a diode while the programming current is allowed to flow through

5 transistors T<sub>1</sub> and T<sub>2</sub> and into the OEL element. The detected threshold voltage of transistor T<sub>2</sub> is stored by a capacitor C<sub>1</sub> connected between the gate and source terminals of transistor T<sub>2</sub> when transistors T<sub>3</sub> and T<sub>1</sub> are switched OFF. Transistor T<sub>4</sub> is then turned ON by driving 10 waveform V<sub>GP</sub> and the current through the OEL element is now provided by supply V<sub>DD</sub>. If the slope of the output characteristics for transistor T<sub>2</sub> were flat, the reproduced current would be the same as the programmed current for any threshold voltage of T<sub>2</sub> detected and stored in 15 capacitor C<sub>1</sub>. However, by turning ON transistor T<sub>4</sub>, the drain-source voltage of transistor T<sub>2</sub> is pulled up, so a flat output characteristic will maintain the reproduced current at the same level as the programmed current. Note that  $\Delta V_{T2}$  shown in figure 2 is imaginary, not real.

20 It has been used solely to represent the threshold voltage of transistor T<sub>2</sub>.

[0006] A constant current is provided, in theory, during a subsequent active programming stage, which is signified by the time interval t<sub>2</sub> to t<sub>5</sub> in the timing diagram 25 shown in figure 2. The reproduction stage starts at time t<sub>6</sub>.

[0007] The circuit of Figure 2 does provide an improvement over the circuit shown in Figure 1 but variations in the threshold value of the control transistor are 30 not fully compensated and variations in image brightness over the display area of the panel remain.

[0008] The present invention seeks to provide an improved driver circuit. In its application to OEL elements the present invention seeks to provide an improved pixel 35 driver circuit in which variations in the threshold voltages of the pixel driver transistor can be further compensated, thereby providing a more uniform pixel brightness over the display area of the panel and, therefore, improved image quality.

[0009] According to a first aspect of the present invention there is provided a driver circuit for a current driven element, the circuit comprising an n-channel transistor and a complementary p-channel transistor connected so as to operatively control, in combination, the current supplied to the current driven element.

[0010] Beneficially, the current driven element is an electroluminescent element.

[0011] Preferably, the driver circuit also comprises respective storage capacitors for the n-channel and p-channel transistors and respective switching means connected so as to establish when operative respective paths to the n-channel and p-channel transistors for respective data voltage pulses.

[0012] Advantageously, the driver circuit may also 55 comprise respective storage capacitors for storing a respective operating voltage of the n-channel and the p-channel transistors during a programming stage, a first switching means connected so as to establish when op-

erative a first current path from a source of current data signals through the n-channel and p-channel transistors and the current driven element during the programming stage, and a second switching means connected to establish when operative a second current path through the n-channel and p-channel transistors and the current driven element during a reproduction stage.

[0013] In a further embodiment, the first switching means and the source of current data signals are connected so as to provide when operative a current source for the current driven element.

[0014] In an alternative embodiment, the first switching means the source of current data signals are connected so as to provide when operative a current sink for the current driven element.

[0015] According to a second aspect of the present invention there is also provided a method of controlling the supply current to a current driven element comprising providing an n-channel transistor and a p-channel transistor connected so as to operatively control, in combination, the supply current to the current driven element.

[0016] Preferably, the method further comprises providing respective storage capacitors for the n-channel and p-channel transistors and respective switching means connected so as to establish when operative respective paths to the n-channel and p-channel transistors for respective data voltage pulses thereby to establish, when operative, a voltage driver circuit for the current driven element.

[0017] Advantageously, the method may comprise providing a programming stage during which the n-channel and p-channel transistors are operated in a first mode and wherein a current path from a source of current data signals is established through the n-channel and the p-channel transistors and the current driven element and wherein a respective operating voltage of the n-channel transistor and the p-channel transistor is stored in respective storage capacitors, and a reproduction stage wherein a second mode and a second current path is established through the n-channel transistor and the p-channel transistor and the current driven element.

[0018] Beneficially, the present invention provides a method of controlling the supply current to an electroluminescent display comprising the method of the invention as described above wherein the current driven element is an electroluminescent element.

[0019] According to a third aspect of the present invention, there is also provided an organic electroluminescent display device comprising a driver circuit as claimed in any one of claims 1 to 12.

[0020] The present invention will now be described by way of further example only and with reference to the accompanying drawings in which:-

Fig. 1 shows a conventional OEL element pixel driver circuit using two transistors;

Fig. 2 shows a known current programmed OEL el-

ement driver circuit with threshold voltage compensation;

Fig. 3 illustrates the concept of a driver circuit including a complementary pair of driver transistors for providing threshold voltage compensation in accordance with the present invention;

Fig. 4 shows plots of characteristics for the complementary driver transistors illustrated in Fig. 3 for various levels of threshold voltages;

Fig. 5 shows a driver circuit arranged to operate as a voltage driver circuit in accordance with a first embodiment of the present invention.

Fig. 6 shows a driver circuit arranged to operate as a current programmed driver circuit in accordance with a second embodiment of the present invention;

Fig. 7 shows a current programmed driver circuit in accordance with a third embodiment of the present invention;

Figs 8 to 11 show SPICE simulation results for the circuit illustrated in Fig. 6;

Fig. 12 is a schematic sectional view of a physical implementation of an OEL element and driver according to an embodiment of the present invention;

Fig. 13 is a simplified plan view of an OEL element/OEL display panel incorporating the present invention;

Fig. 14 is a schematic view of a mobile personal computer incorporating a display device having a driver according to the present invention;

Fig. 15 is a schematic view of a mobile telephone incorporating a display device having a driver according to the present invention,

Fig. 16 is a schematic view of a digital camera incorporating a display device having a driver according to the present invention,

Fig. 17 illustrates the application of the driver circuit of the present invention to a magnetic RAM, and

Fig. 18 illustrates an alternative application of the driver circuit of the present invention to a magnetic RAM, and

Fig. 19 illustrates the application of the driver circuit of the present invention to a magnetoresistive element.

[0021] The concept of a driver circuit according to the present invention is illustrated in Fig. 3. An OEL element is coupled between two transistors  $T_{12}$  and  $T_{15}$  which operate, in combination, as an analog current control for the current flowing through the OEL element. Transistor  $T_{12}$  is a p-channel transistor and transistor  $T_{15}$  is an n-channel transistor which act therefore, in combination, as a complementary pair for analog control of the current through the OEL element.

[0022] As mentioned previously, one of the most important parameters in a TFT analog circuit design is the threshold voltage  $V_T$ . Any variation,  $\Delta V_T$  within a circuit has a significant effect on the overall circuit performance. Variations in the threshold voltage can be viewed

as a rigid horizontal shift of the source to drain current versus the gate to source voltage characteristic for the transistor concerned and are caused by the interface charge at the gate of the transistor.

[0023] It has been realised with the present invention that in an array of TFT devices, in view of the fabrication techniques employed, neighbouring or relatively close TFT's have a high probability of exhibiting the same or an almost similar value of threshold voltage  $\Delta V_T$ . Furthermore, it has been realised that as the effects of the same  $\Delta V_T$  on p-channel and n-channel TFT's are complementary, compensation for variations in threshold voltage  $\Delta V_T$  can be achieved by employing a pair of TFT's, one p-channel TFT and one n-channel TFT, to provide analog control of the driving current flowing to the OEL element. The driving current can, therefore, be provided independently of any variation of the threshold voltage. Such a concept is illustrated in figure 3.

[0024] Figure 4 illustrates the variation in drain current, that is the current flowing through the OEL element shown in figure 3, for various levels of threshold voltage  $\Delta V_T$ ,  $\Delta V_{T1}$ ,  $\Delta V_{T2}$  for the transistors  $T_{12}$  and  $T_{15}$ . Voltages  $V_1$ ,  $V_2$  and  $V_D$  are respectively the voltages appearing across transistor  $T_{12}$ ,  $T_{15}$  and the OEL element from a voltage source  $V_{DD}$ . Assuming that the transistors  $T_{12}$  and  $T_{15}$  have the same threshold voltage and assuming that  $\Delta V_T = 0$ , then the current flowing through the OEL element is given by cross-over point A for the characteristics for the p-channel transistor  $T_{12}$  and the n-channel transistor  $T_{15}$  shown in figure 4. This is shown by value  $I_0$ .

[0025] Assuming now that the threshold voltage of the p-channel and n-channel transistors changes to  $\Delta V_{T1}$ , the OEL element current  $I_1$  is then determined by cross-over point B. Likewise, for a variation in threshold voltage to  $\Delta V_2$ , the OEL element current  $I_2$  is given by cross-over point C. It can be seen from figure 4 that even with the variations in the threshold voltage there is minimal variation in the current flowing through the OEL element.

[0026] Figure 5 shows a pixel driver circuit configured as a voltage driver circuit. The circuit comprises p-channel transistor  $T_{12}$  and n-channel transistor  $T_{15}$  acting as a complementary pair to provide, in combination, an analog current control for the OEL element. The circuit includes respective storage capacitors  $C_{12}$  and  $C_{15}$  and respective switching transistors  $T_A$  and  $T_B$  coupled to the gates of transistors  $T_{12}$  and  $T_{15}$ . When transistors  $T_A$  and  $T_B$  are switched ON data voltage signals  $V_1$  and  $V_2$  are stored respectively in storage capacitors  $C_{12}$  and  $C_{15}$  when the pixel is not addressed. The transistors  $T_A$  and  $T_B$  function as pass gates under the selective control of addressing signals  $\phi_1$  and  $\phi_2$  applied to the gates of transistors  $T_A$  and  $T_B$ .

[0027] Figure 6 shows a driver circuit according to the present invention configured as a current programmed OEL element driver circuit. As with the voltage driver circuit, p-channel transistor  $T_{12}$  and n-channel transistor  $T_{15}$  are coupled so as to function as an analog current

control for the OEL element. Respective storage capacitors  $C_1$ ,  $C_2$  and respective switching transistors  $T_1$  and  $T_6$  are provided for transistors  $T_{12}$  and  $T_{15}$ . The driving waveforms for the circuit are also shown in figure 6. Either transistors  $T_1$ ,  $T_3$  and  $T_6$ , or transistor  $T_4$  can be ON at any one time. Transistors  $T_1$  and  $T_6$  connect respectively between the drain and gate of transistors  $T_{12}$  and  $T_{15}$  and switch in response to applied waveform  $V_{SEL}$  to toggle transistors  $T_{12}$  and  $T_{15}$  to act either as diodes or as transistors in saturation mode. Transistor  $T_3$  is also connected to receive waveform  $V_{SEL}$ . Transistors  $T_1$  and  $T_6$  are both p-channel transistors to ensure that the signals fed through these transistors are at the same magnitude. This is to ensure that any spike currents through the OEL element during transitions of the waveform  $V_{SEL}$  are kept to a minimum.

[0028] The circuit shown in figure 6 operates in a similar manner to known current programmed pixel driver circuits in that a programming stage and a display stage are provided in each display period but with the added benefit that the drive current to the OEL element is controlled by the complementary opposite channel transistors  $T_{12}$  and  $T_{15}$ . Referring to the driving waveforms shown in figure 6, a display period for the driver circuit extends from time  $t_0$  to time  $t_6$ . Initially, transistor  $T_4$  is ON and transistors  $T_1$ ,  $T_3$  and  $T_6$  are OFF. Transistor  $T_4$  is turned OFF at time  $t_1$  by the waveform  $V_{GP}$  and transistors  $T_1$ ,  $T_3$  and  $T_6$  are turned ON at time  $t_3$  by the waveform  $V_{SEL}$ . With transistors  $T_1$  and  $T_6$  turned ON, the p-channel transistor  $T_{12}$  and the complementary n-channel transistor  $T_{15}$  act in a first mode as diodes. The driving waveform for the frame period concerned is available from the current source  $I_{DAT}$  at time  $t_2$  and this is passed by the transistor  $T_3$  when it switches on at time  $t_3$ . The detected threshold voltages of transistors  $T_{12}$  and  $T_{15}$  are stored in capacitors  $C_1$  and  $C_2$ . These are shown as imaginary voltage sources  $\Delta V_{T12}$  and  $\Delta V_{T15}$  in figure 6.

[0029] Transistors  $T_1$ ,  $T_3$  and  $T_6$  are then switched OFF at time  $t_4$  and transistor  $T_4$  is switched ON at time  $t_5$  and the current through the OEL element is then provided from the source  $V_{DD}$  under the control of the p-channel and n-channel transistors  $T_{12}$  and  $T_{15}$  operating in a second mode, i.e. as transistors in saturation mode. It will be appreciated that as the current through the OEL element is controlled by the complementary p-channel and n-channel transistors  $T_{12}$  and  $T_{15}$ , any variation in threshold voltage in one of the transistors will be compensated by the other opposite channel transistor, as described previously with respect to figure 4.

[0030] In the current programmed driver circuit shown in figure 6, the switching transistor  $T_3$  is coupled to the p-channel transistor  $T_{12}$ , with the source of the driving waveform  $I_{DAT}$  operating as a current source. However, the switching transistor  $T_3$  may as an alternative be coupled to the n-channel transistor  $T_{15}$  as shown in figure 7, whereby  $I_{DAT}$  operates as a current sink. In all other respects the operation of the circuit shown in figure 7 is

the same as for the circuit shown in figure 6.

[0031] Figures 8 to 11 show a SPICE simulation of an improved pixel driver circuit according to the present invention.

[0032] Referring to figure 8, this shows the driving waveforms  $I_{DAT}$ ,  $V_{GP}$ ,  $V_{SEL}$  and three values of threshold voltage, namely -1volt, 0volts and +1volt used for the purposes of simulation to show the compensating effect provided by the combination of the p-channel and n-channel transistors for controlling the current through the OEL element. From figure 8, it can be seen that, initially the threshold voltage  $\Delta V_T$  was set at -1volt, increasing to 0volts at  $0.3 \times 10^{-4}$  seconds and increasing again to +1volt at  $0.6 \times 10^{-4}$  seconds. However, it can be seen from figure 9 that even with such variations in the threshold voltage the driving current through the OEL element remains relatively unchanged.

[0033] The relative stability in the driving current through the OEL element can be more clearly seen in figure 10, which shows a magnified version of the response plots shown in figure 9.

[0034] It can be seen from figure 10 that, using a value of 0 volts as a base for the threshold voltage  $\Delta V_T$ , that if the threshold voltage  $\Delta V_T$  changes to -1volts there is a change of approximately 1.2% in the drive current through the OEL element and if the threshold voltage  $\Delta V_T$  is changed to +1volt, there is a reduction in drive current of approximately 1.7% compared to the drive current when the threshold voltage  $\Delta V_T$  is 0 volts. The variation of drive current of 8.7% is shown for reference purposes only as such a variation can be compensated by gamma correction, which is well known in this art and will not therefore be described in relation to the present invention.

[0035] Figure 11 shows that for levels of  $I_{DAT}$  ranging from  $0.2\mu A$  to  $1.0\mu A$ , the improved control of the OEL element drive current is maintained by the use of the p-channel and opposite n-channel transistors in accordance with the present invention.

[0036] It will be appreciated from the above description that the use of a p-channel transistor and an opposite n-channel transistor to provide, in combination, analog control of the drive current through an electroluminescent device provides improved compensation for the effects which would otherwise occur with variations in the threshold voltage of a single p-channel or n-channel transistor.

[0037] Preferably, the TFT n-channel and p-channel transistors are fabricated as neighbouring or adjacent transistors during the fabrication of an OEL element/OEL display so as to maximise the probability of the complementary p-channel and n-channel transistors having the same value of threshold voltage  $\Delta V_T$ . The p-channel and n-channel transistors may be further matched by comparison of their output characteristics.

[0038] Figure 12 is a schematic cross-sectional view of the physical implementation of the pixel driver circuit in an OEL element structure. In figure 12, numeral 132

indicates a hole injection layer, numeral 133 indicates an organic EL layer, and numeral 151 indicates a resist or separating structure. The switching thin-film transistor 121 and the n-channel type current-thin-film transistor 122 adopt the structure and the process ordinarily used for a low-temperature polysilicon thin-film transistor, such as are used for example in known thin-film transistor liquid crystal display devices such as a top-gate structure and a fabrication process wherein the maximum temperature is 600°C or less. However, other structures and processes are applicable.

[0039] The forward oriented organic EL display element 131 is formed by: the pixel electrode 115 formed of Al, the opposite electrode 116 formed of ITO, the hole injection layer 132, and the organic EL layer 133. In the forward oriented organic EL display element 131, the direction of current of the organic EL display device can be set from the opposite electrode 116 formed of ITO to the pixel electrode 115 formed of Al.

[0040] The hole injection layer 132 and the organic EL layer 133 may be formed using an ink-jet printing method, employing the resist 151 as a separating structure between the pixels. The opposite electrode 116 formed of ITO may be formed using a sputtering method. However, other methods may also be used for forming all of these components.

[0041] The typical layout of a full display panel employing the present invention is shown schematically in figure 13. The panel comprises an active matrix OEL

element 200 with analogue current program pixels, an integrated TFT scanning driver 210 with level shifter, a flexible TAB tape 220, and an external analogue driver LSI 230 with an integrated RAM/controller. Of course, this is only one example of the possible panel arrangements in which the present invention can be used.

[0042] The structure of the organic EL display device is not limited to the one described here. Other structures are also applicable.

[0043] The improved pixel driver circuit of the present invention may be used in display devices incorporated in many types of equipment such as mobile displays e.g. mobile phones, laptop personal computers, DVD players, cameras, field equipment; portable displays such as desktop computers, CCTV or photo albums; or industrial displays such as control room equipment displays.

[0044] Several electronic apparatuses using the above organic electroluminescent display device will now be described.

[0045] An example in which the display device according to one of the above embodiments is applied to a mobile personal computer will now be described.

[0046] Figure 14 is an isometric view illustrating the configuration of this personal computer. In the drawing, the personal computer 1100 is provided with a body

1104 including a keyboard 1102 and a display unit 1106. The display unit 1106 is implemented using a display panel fabricated according to the present invention, as described above.

<2: Portable Phone>

[0047] Next, an example in which the display device is applied to a display section of a portable phone will be described. Fig. 15 is an isometric view illustrating the configuration of the portable phone. In the drawing, the portable phone 1200 is provided with a plurality of operation keys 1202, an earpiece 1204, a mouthpiece 1206, and a display panel 100. This display panel 100 is implemented using a display panel fabricated according to the present invention, as described above.

<3: Digital Still Camera>

[0048] Next, a digital still camera using an OEL display device as a finder will be described. Fig. 16 is an isometric view illustrating the configuration of the digital still camera and the connection to external devices in brief.

[0049] Typical cameras sensitise films based on optical images from objects, whereas the digital still camera 1300 generates imaging signals from the optical image of an object by photoelectric conversion using, for example, a charge coupled device (CCD). The digital still camera 1300 is provided with an OEL element 100 at the back face of a case 1302 to perform display based on the imaging signals from the CCD. Thus, the display panel 100 functions as a finder for displaying the object. A photo acceptance unit 1304 including optical lenses and the CCD is provided at the front side (behind in the drawing) of the case 1302.

[0050] When a cameraman determines the object image displayed in the OEL element panel 100 and releases the shutter, the image signals from the CCD are transmitted and stored to memories in a circuit board 1308. In the digital still camera 1300, video signal output terminals 1312 and input/output terminals 1314 for data communication are provided on a side of the case 1302. As shown in the drawing, a television monitor 1430 and a personal computer 1440 are connected to the video signal terminals 1312 and the input/output terminals 1314, respectively, if necessary. The imaging signals stored in the memories of the circuit board 1308 are output to the television monitor 1430 and the personal computer 1440, by a given operation.

[0051] Examples of electronic apparatuses, other than the personal computer shown in Fig. 14, the portable phone shown in Fig. 15, and the digital still camera shown in Fig. 16, include OEL element television sets, view-finder-type and monitoring-type video tape recorders, car navigation systems, pagers, electronic notebooks, portable calculators, word processors, workstations, TV telephones, point-of-sales system (POS) ter-

minals, and devices provided with touch panels. Of course, the above OEL device can be applied to display sections of these electronic apparatuses.

[0052] The driver circuit of the present invention can be disposed not only in a pixel of a display unit but also in a driver disposed outside a display unit.

[0053] In the above, the driver circuit of the present invention has been described with reference to various display devices. The applications of the driver circuit of the present invention are much broader than just display devices and include, for example, its use with a magnetoresistive RAM, a capacitance sensor, a charge sensor, a DNA sensor, a night vision camera and many other devices.

[0054] Figure 17 illustrates the application of the driver circuit of the present invention to a magnetic RAM. In figure 17 a magnetic head is indicated by the reference MH.

[0055] Figure 18 illustrates an alternative application of the driver circuit of the present invention to a magnetic RAM. In figure 18 a magnetic head is indicated by the reference MH.

[0056] Figure 19 illustrates the application of the driver circuit of the present invention to a magnetoresistive element. In figure 19 a magnetic head is indicated by the reference MH, and a magnetic resistor is indicated by the reference MR.

[0057] The foregoing description has been given by way of example only and it will be appreciated by a person skilled in the art that modifications can be made without departing from the scope of the present invention.

35 Claims

1. A driver circuit for a current driven element, the circuit comprising an n-channel transistor and a complementary p-channel transistor connected so as to operatively control, in combination, the current supplied to the current driven element.
2. A driver circuit as claimed in claim 1, wherein the complementary n-channel and p-channel transistors comprise polysilicon thin film transistors.
3. A driver circuit as claimed in claim 2, wherein the complementary n-channel and p-channel transistors are spatially arranged in close proximity to each other for providing a complementary pair of n-channel and p-channel transistors having approximately equal threshold voltages.
4. A driver circuit as claimed in any one of claims 1 to 3 connected so as to establish when operative a voltage driver circuit comprising respective storage capacitors for the n-channel and p-channel transistors and respective switching means connected so

as to establish when operative respective paths to the n-channel and p-channel transistors for respective data voltage pulses.

5. A driver circuit as claimed in any one of claims 1 to 3 comprising respective storage capacitors for storing a respective operating voltage of the n-channel and the p-channel transistors during a programming stage, a first switching means connected so as to establish when operative a first current path from a source of current data signals through the n-channel and p-channel transistors and the current driven element during the programming stage, and a second switching means connected to establish when operative a second current path through the n-channel and p-channel transistors and the current driven element during a reproduction stage.
6. A driver circuit as claimed in claim 5, wherein the first switching means and the source of current data signals are connected so as to provide when operative a current source for the current driven element.
7. A driver circuit as claimed in claim 5, wherein the first switching means and the source of current data signals are connected so as to provide when operative a current sink for the current driven element.
8. A driver circuit as claimed in any one of claims 5 to 7, further comprising respective further switching means respectively connected to bias the n-channel transistor and the p-channel transistor to act as diodes during the programming stage.
9. A driver circuit as claimed in claim 8, wherein the respective further switching means comprise p-channel transistors.
10. A driver circuit as claimed in any one of claims 5 to 9, wherein the circuit is implemented with polysilicon thin film transistors.
11. A driver circuit as claimed in claim 4, wherein the circuit is implemented using polysilicon thin film transistors.
12. A driver circuit as claimed in any preceding claim, wherein the current driven element is an electroluminescent element.
13. A method of controlling the supply current to a current driven element comprising providing an n-channel transistor and a p-channel transistor connected so as to operatively control, in combination, the supply current to the current driven element.
14. A method as claimed in claim 13, comprising the further step of providing the n-channel transistor and the p-channel transistor as polysilicon thin film transistors.
- 5 15. A method as claimed in claim 14 comprising the further step of spatially arranging the n-channel and p-channel polysilicon thin film transistors in close proximity to each other.
- 10 16. A method as claimed in any one of claims 13 to 15 comprising providing respective storage capacitors for the n-channel and p-channel transistors and respective switching means connected so as to establish when operative respective paths to the n-channel and p-channel transistors for respective data voltage pulses thereby to establish, when operative, a voltage driver circuit for the current driven element.
- 15 20. 17. A method as claimed in any one of claims 13 to 15 comprising providing a programming stage during which the n-channel and p-channel transistors are operated in a first mode and wherein a current path from a source of current data signals is established through the n-channel and the p-channel transistors and the current driven element and wherein a respective operating voltage of the n-channel transistor and the p-channel transistor is stored in respective storage capacitors, and a reproduction stage wherein a second mode and a second current path is established through the n-channel transistor and the p-channel transistor and the current driven element.
- 25 30. 18. A method as claimed in claim 17, wherein the first mode comprises operating the n-channel and p-channel transistors as diodes.
- 35 19. A method of controlling the supply current to an electroluminescent display comprising the method as claimed in any one of claims 13 to 18 wherein the current driven element is an electroluminescent element.
- 40 45. 20. An organic electroluminescent display device comprising a driver circuit as claimed in any one of claims 1 to 12.
- 50 21. An electronic apparatus incorporating an organic electroluminescent display device as claimed in claim 20.
- 55 22. A circuit comprising a current driven element and at least two active elements, the current driven element being disposed between the two active elements.
23. A circuit comprising a current driven element and at

least two active elements, the two active elements being connected through the current driven element together.

24. The circuit according to claim 22 or claim 23, where- 5  
in the two active elements are transistors.

25. The circuit according to claim 24, wherein the two 10  
transistors are mutually different channel type transis-  
tors.

26. the circuit according to claim 22 or claim 23, wherein 15  
the current driven element is an organic electrolu-  
minescent element.

27. The circuit according to claim 24, wherein the gates 20  
of the two transistors are each connected to a re-  
spective capacitor.

28. An electro-optical device comprising the circuit ac- 20  
cording to claim 22.

29. An electronic apparatus incorporating an electro- 25  
optical device according to claim 28.

30. A method for driving a circuit comprising a current 30  
driven element, a first active element, and a second  
active element that is disposed at a side of the cur-  
rent driven element opposite to the first active ele-  
ment, controlling a current supplied to the current  
driven element by the first active element and the  
second active element.

31. The method according to claim 30, comprising the 35  
step of selecting the first active element to be a first  
transistor and selecting the second active element  
to be a second transistor.

32. The method according to claim 31, comprising a 40  
step of determining a gate voltage of at least one of  
the first transistor and the second transistor based  
on a predetermined current.

33. The method according to claim 32, comprising the 45  
step of causing the predetermined current to flow  
through a second current path different from a first  
current path that includes the current driven ele-  
ment.

34. The method according to claim 33, comprising the 50  
step of arranging the second current path to include  
at least one of the first transistor and the second  
transistor.

Fig.1.

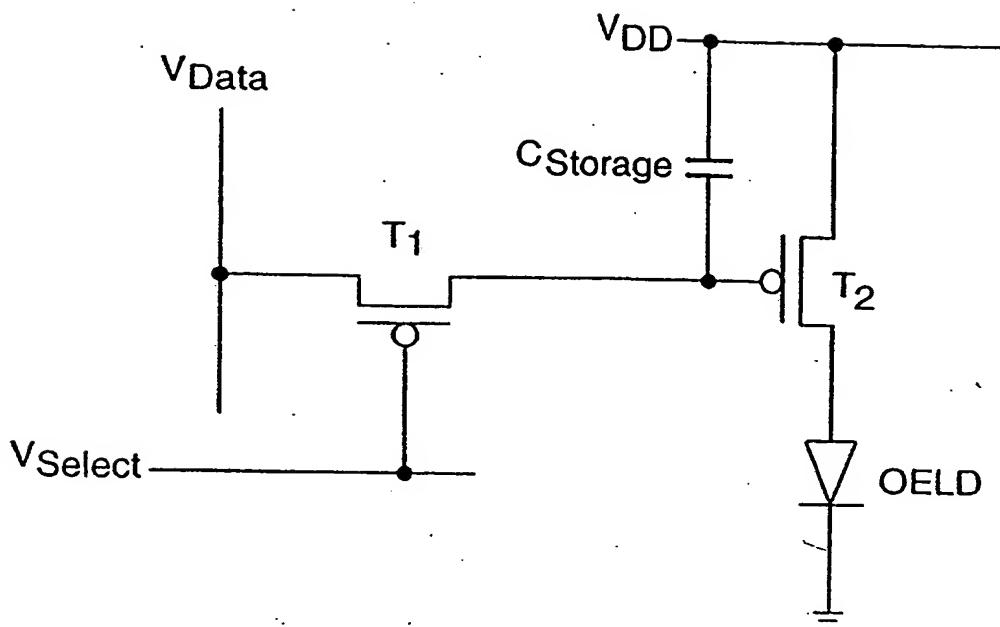


Fig.2.

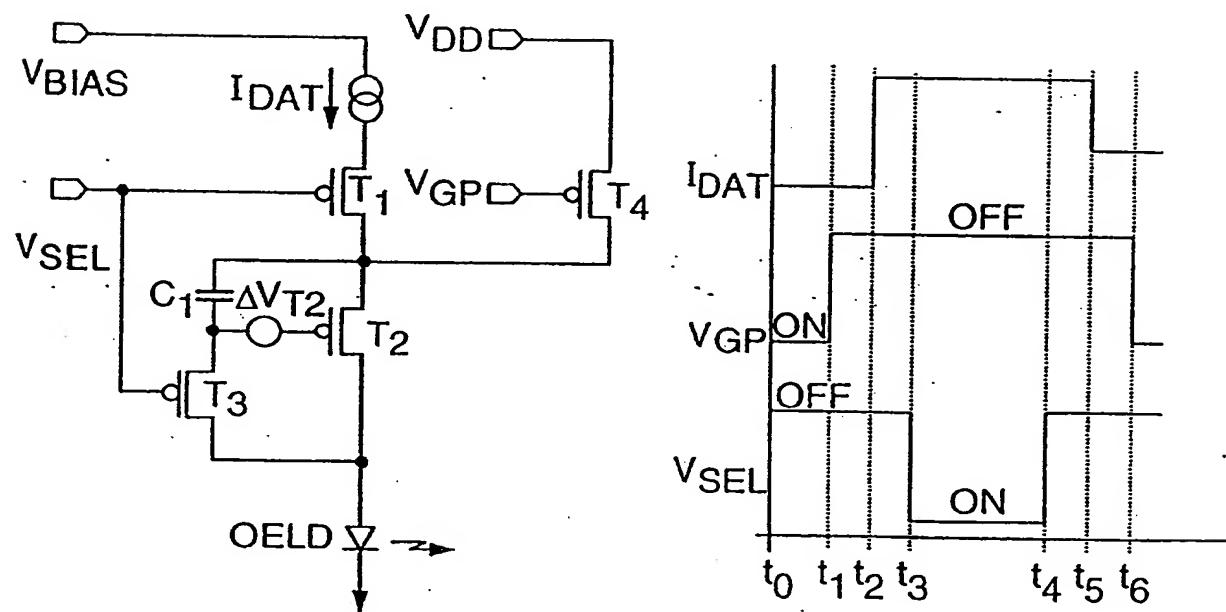


Fig.3.

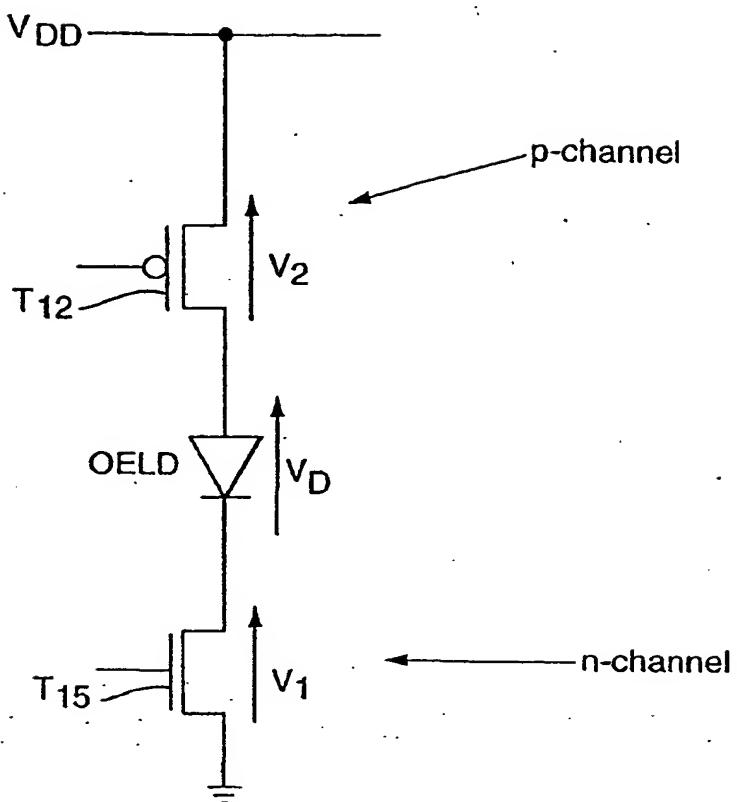


Fig.4.

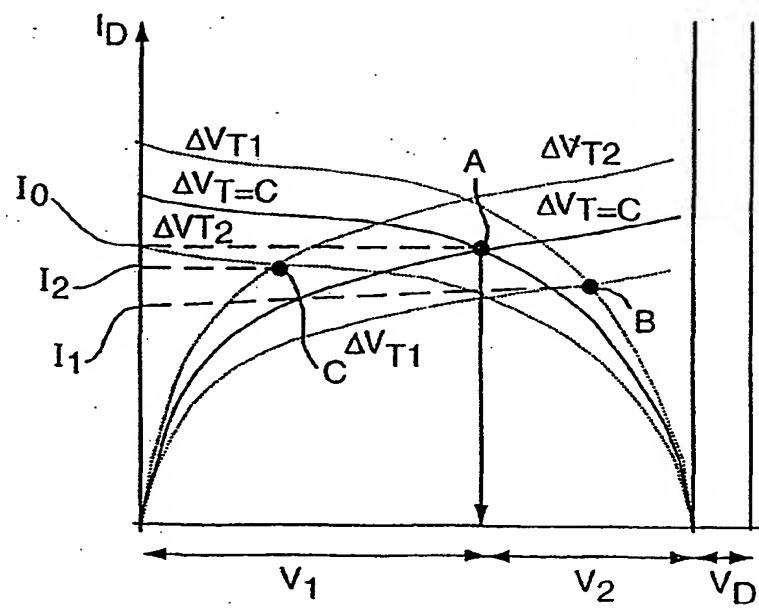


Fig.5.

Data  
Voltages

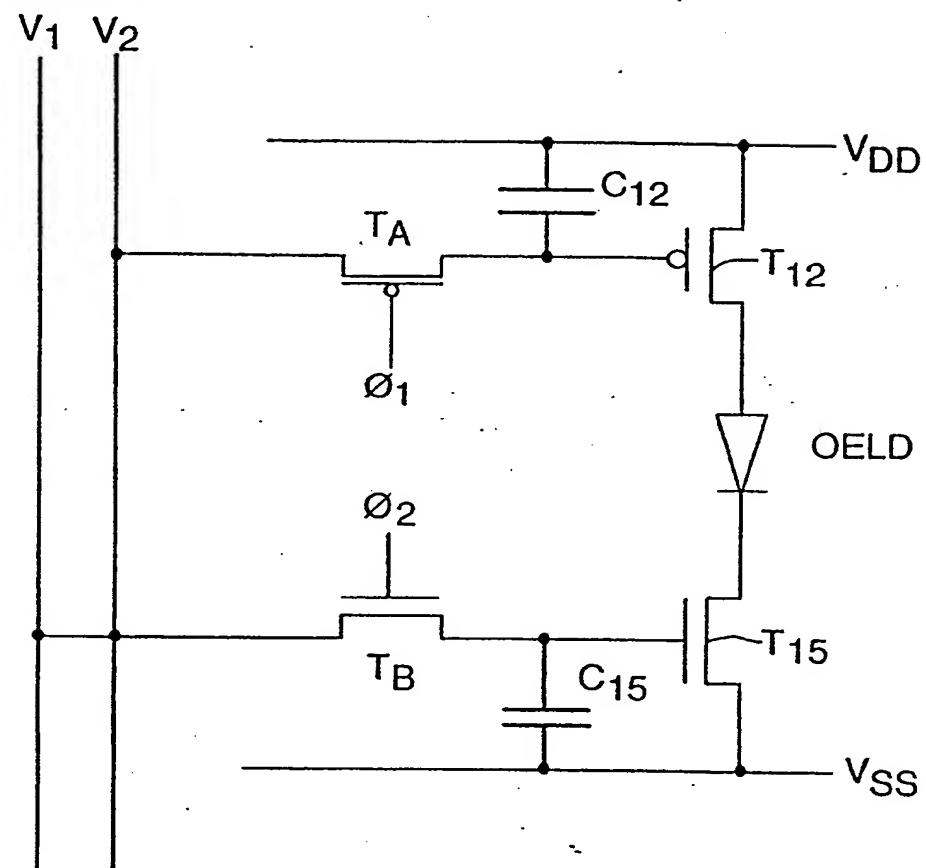
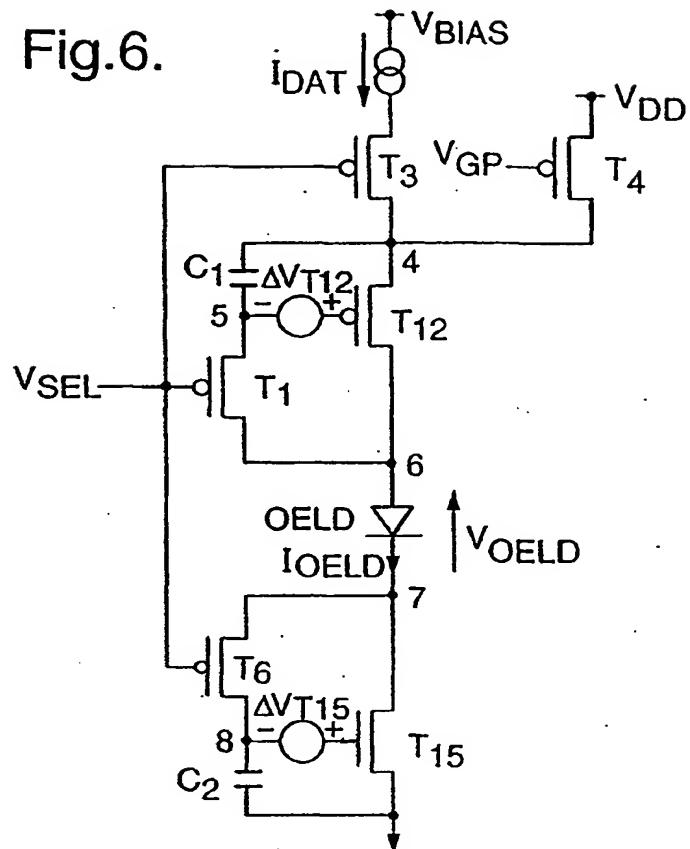


Fig.6.



## DRIVING WAVEFORM

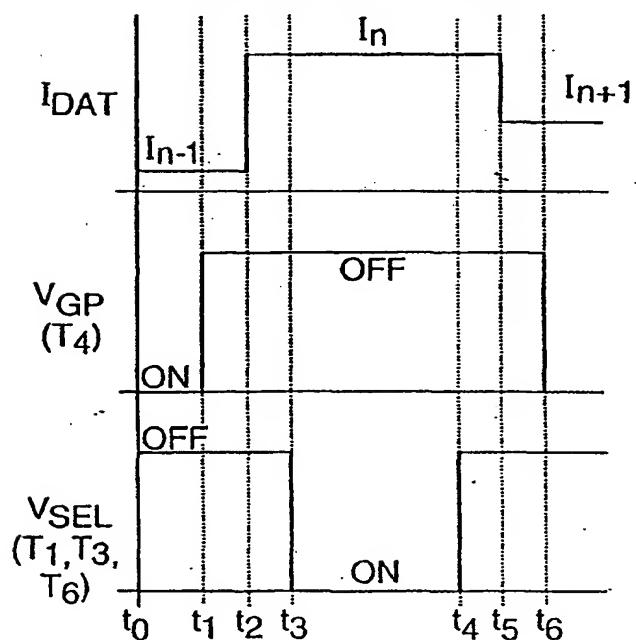
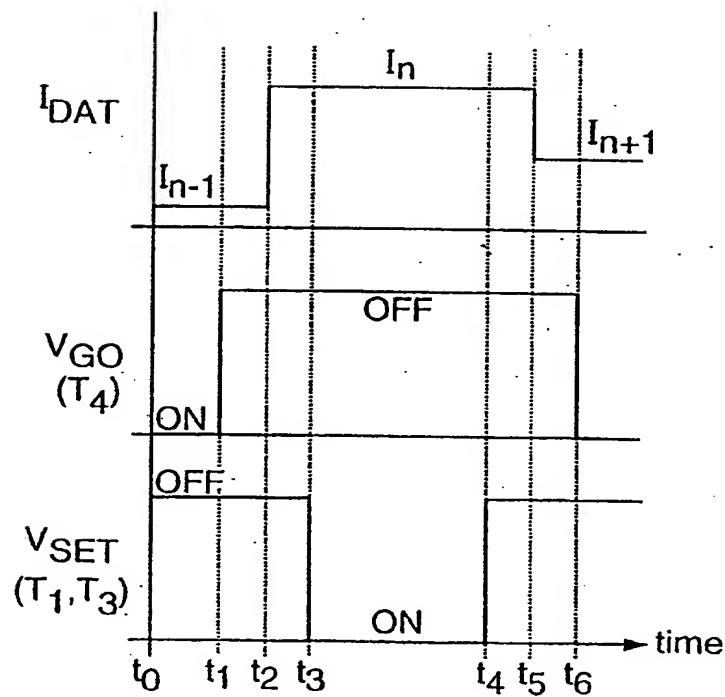
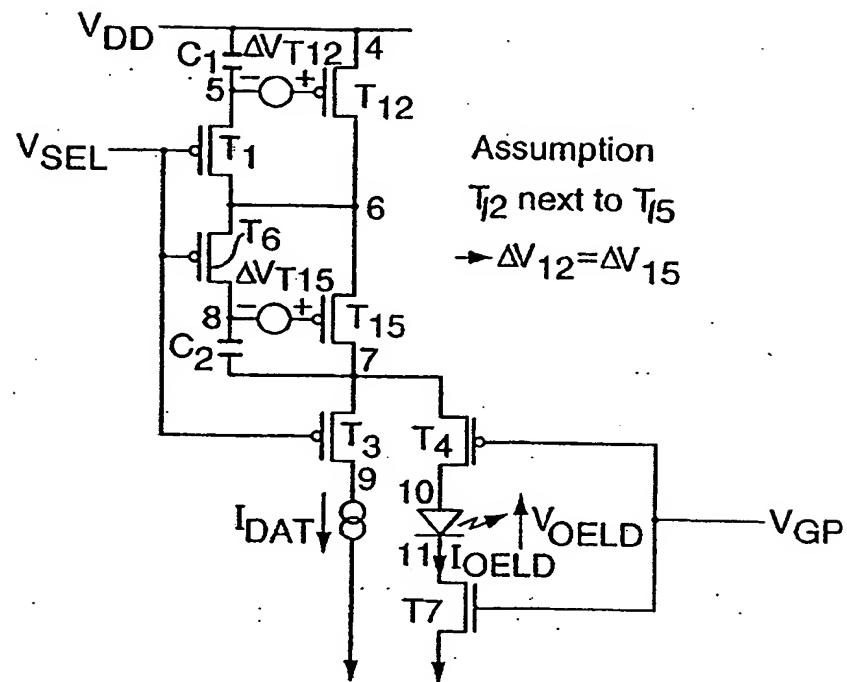
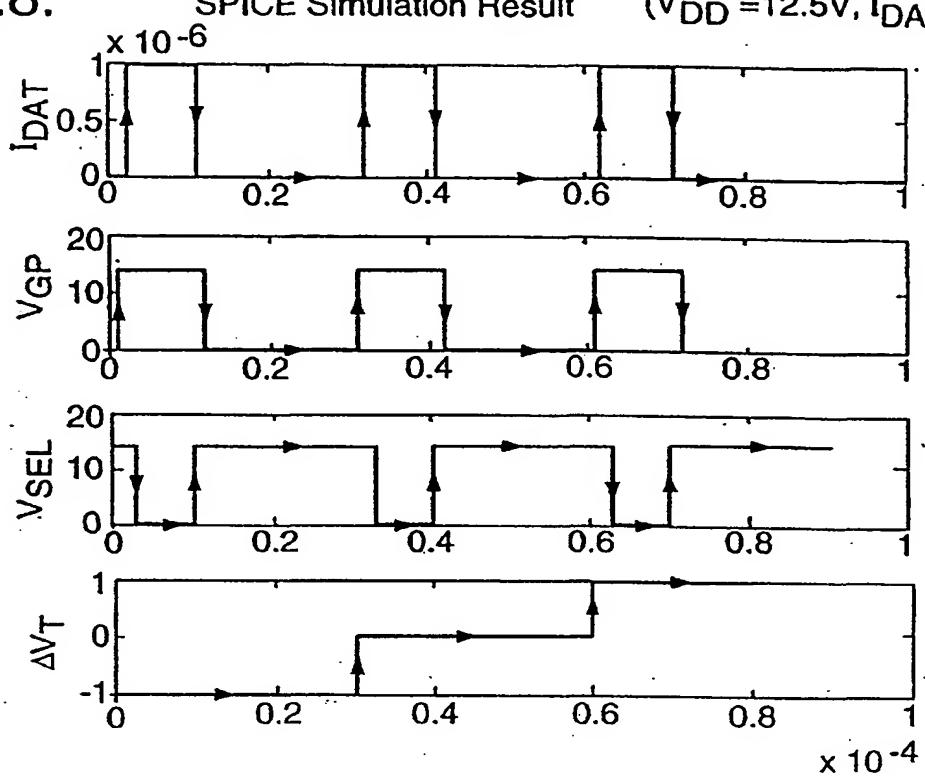


Fig.7.



**Fig.8.** SPICE Simulation Result ( $V_{DD} = 12.5V$ ,  $I_{DAT} = 1\mu A$ )



**Fig.9.**

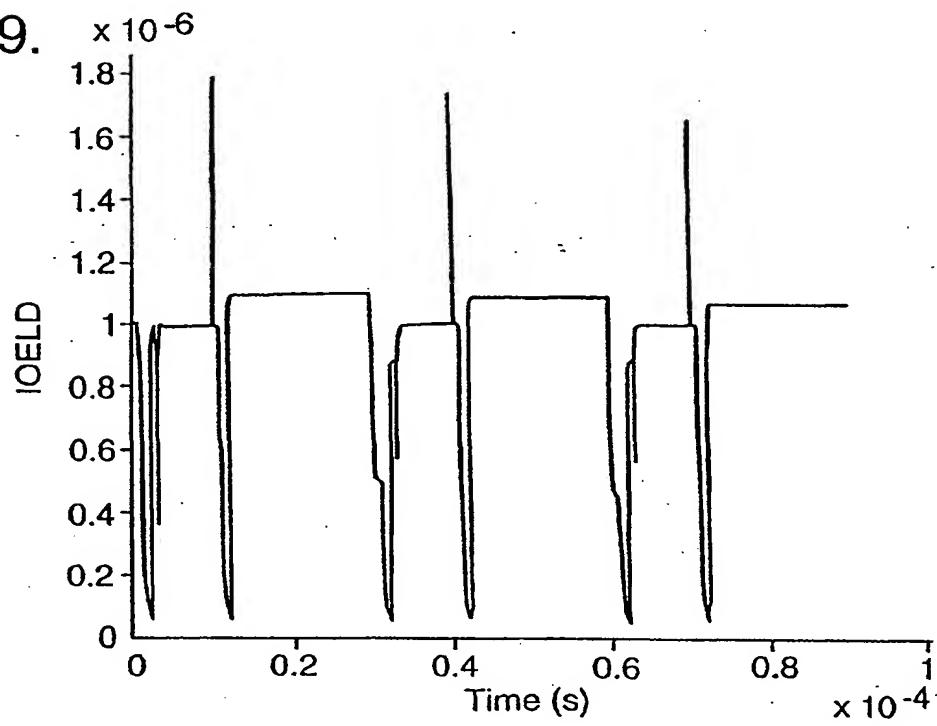


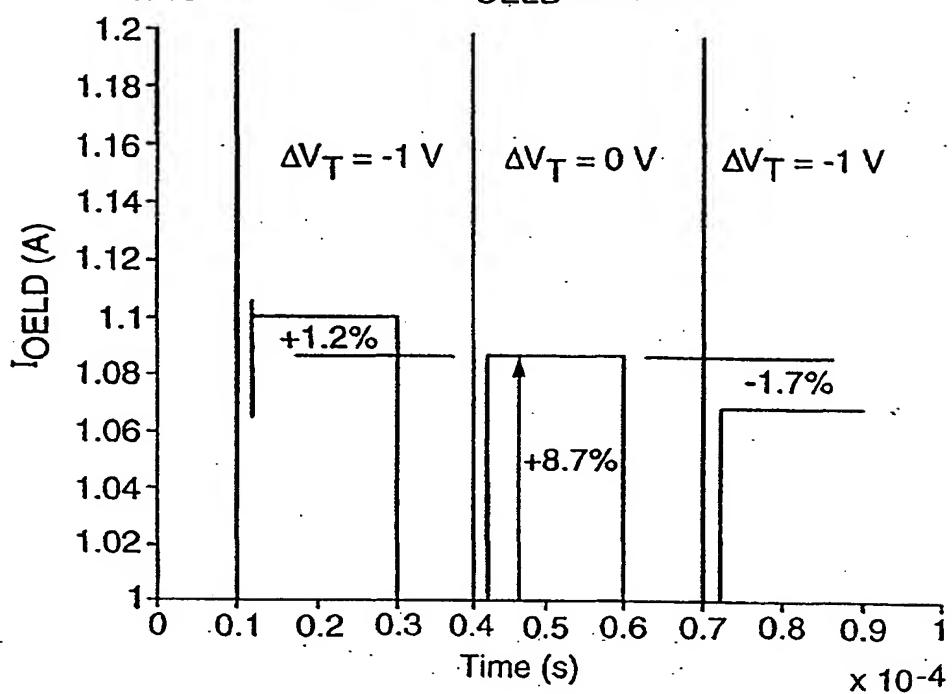
Fig.10. Magnified  $I_{OELD}$  Response

Fig.11.

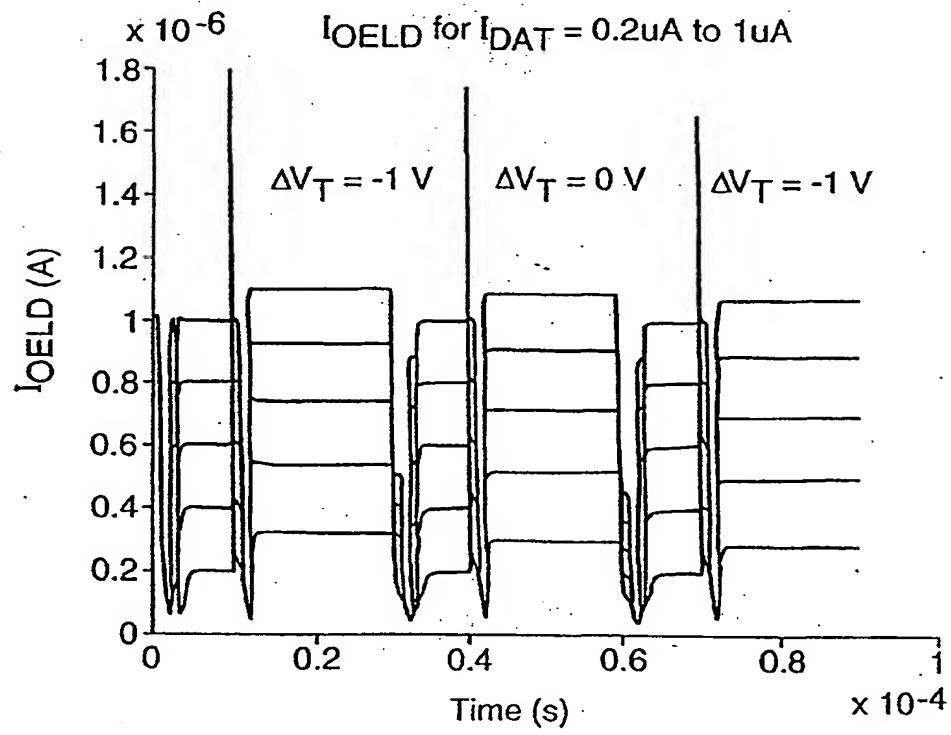


Figure 12

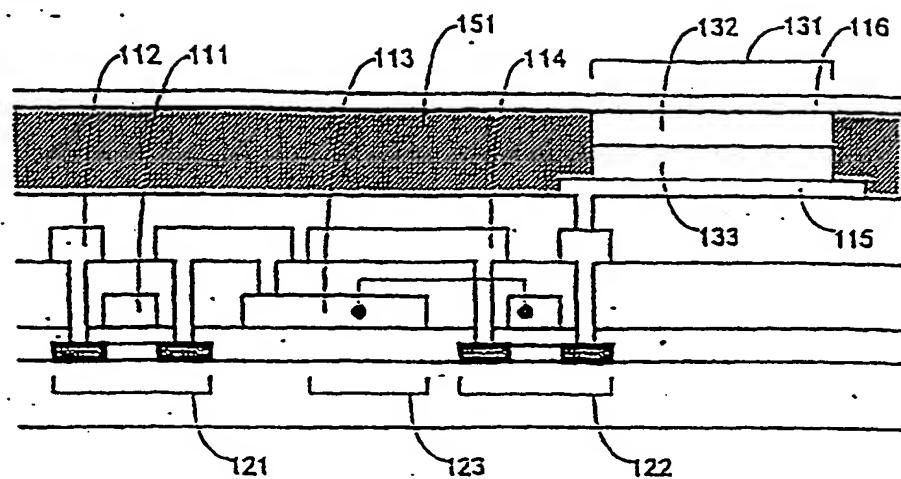


Figure 13

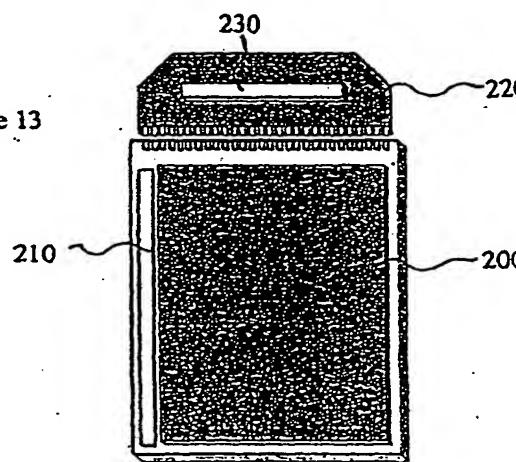
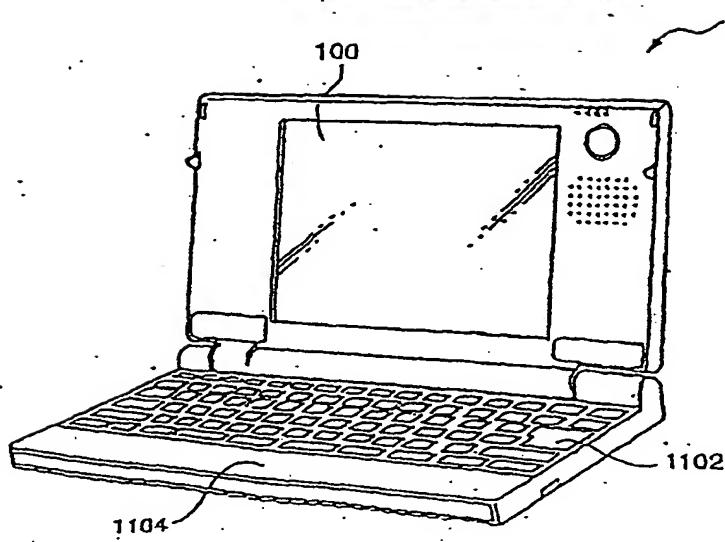


Figure 14



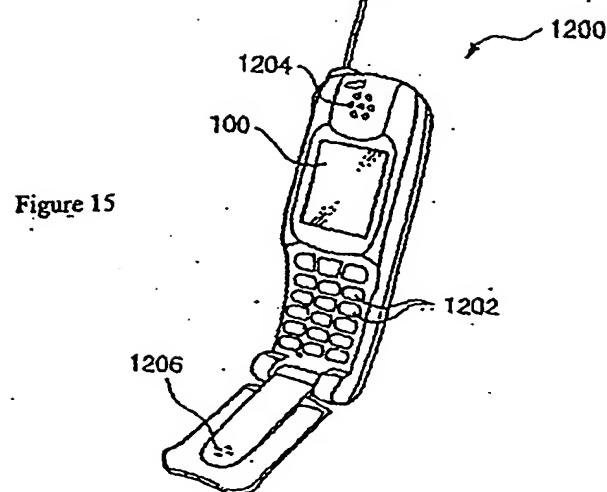


Figure 15

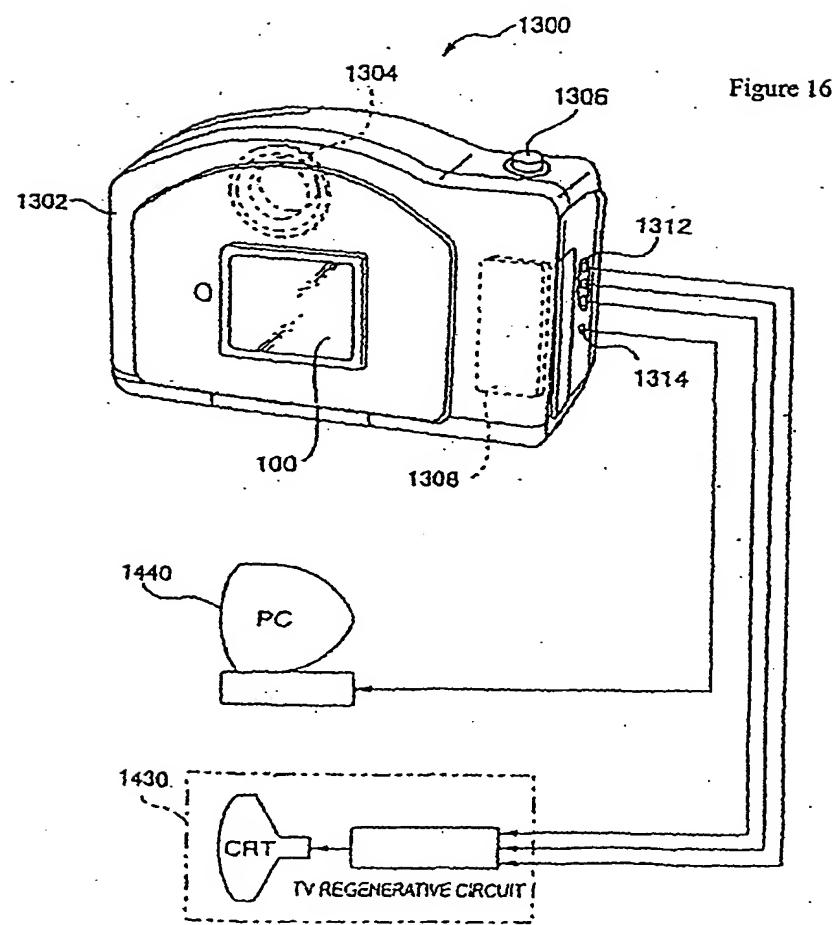


Figure 16

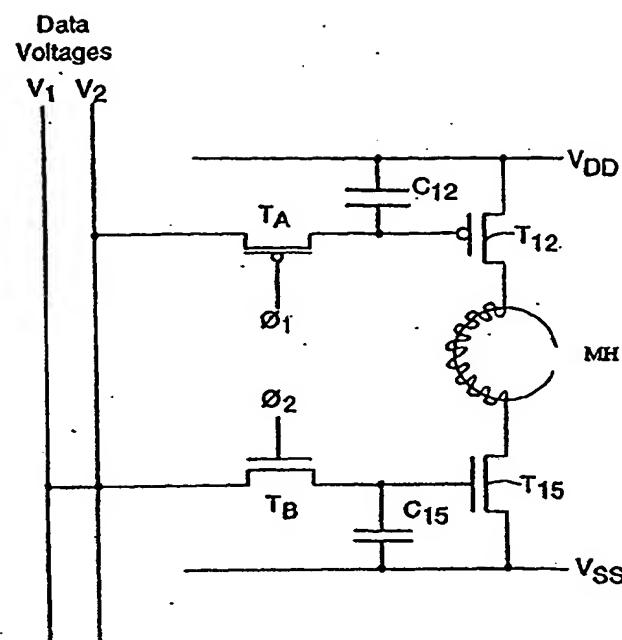


Figure 17

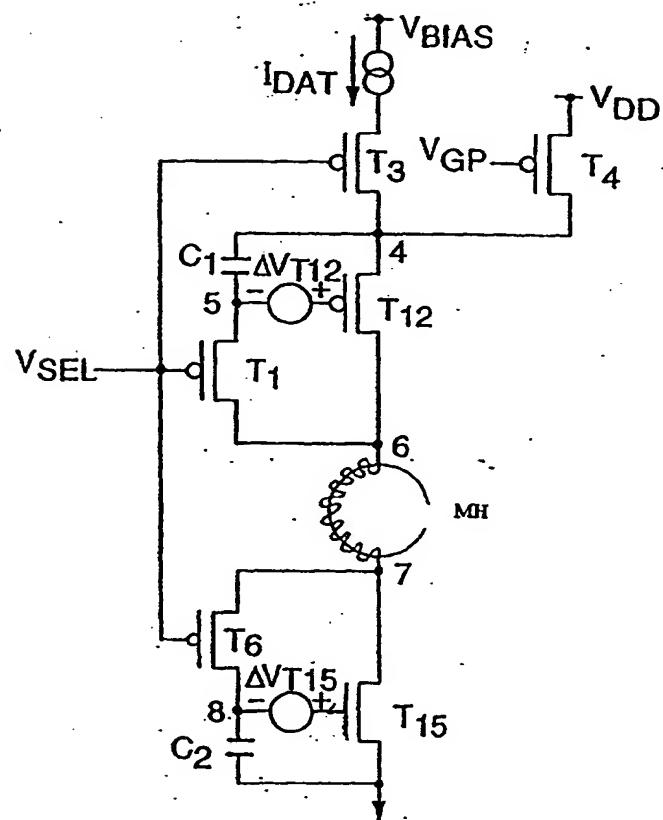
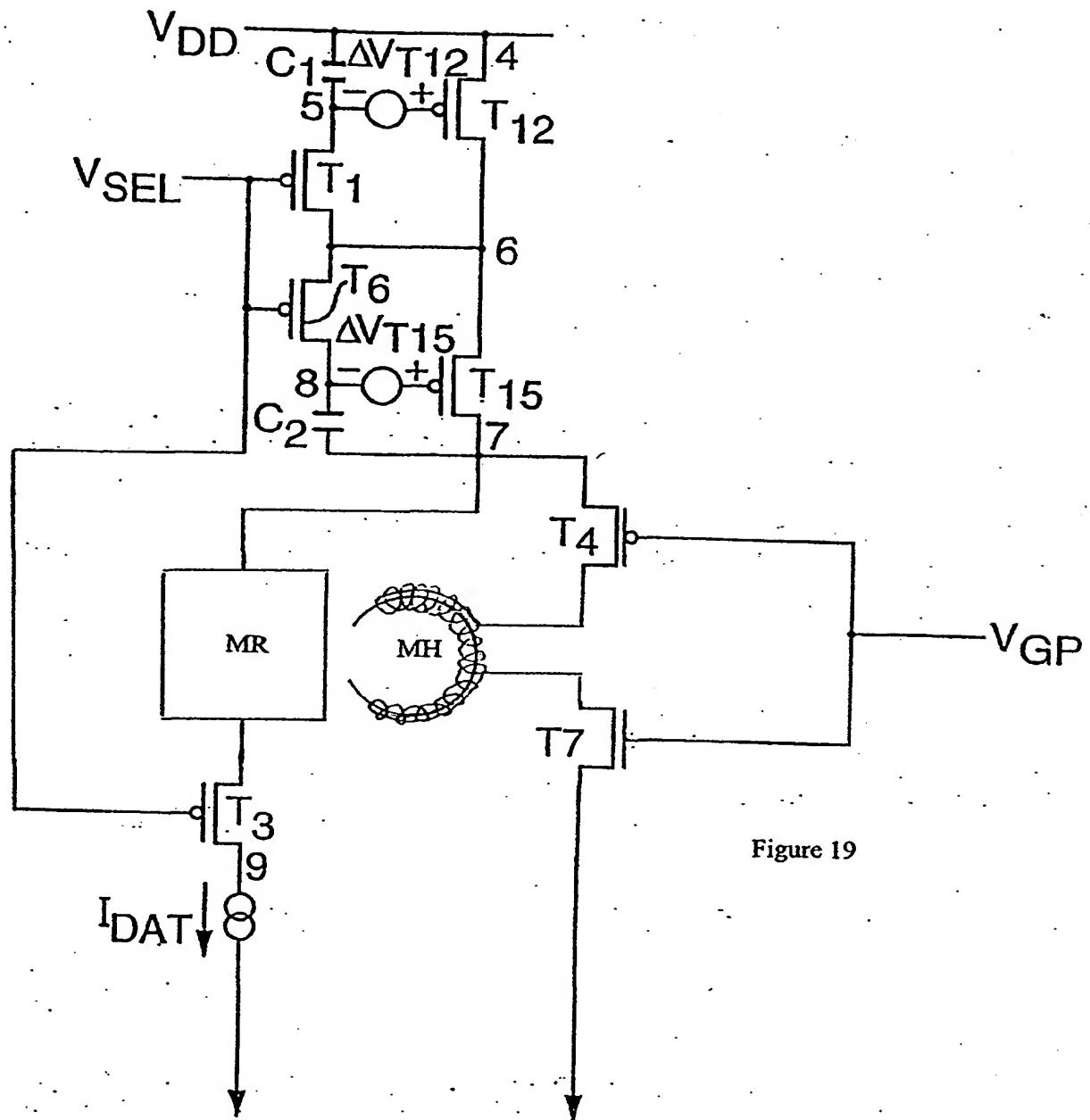


Figure 18



**Figure 19**



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## EUROPEAN SEARCH REPORT

Application Number  
EP 01 30 5904

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Y	see abstract * column 1, line 12 - column 2, line 15 * * column 2, line 61 - column 4, line 53; figures 1-4 *		
A	-----	20,21,26	
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X	see abstract * column 1, line 19 - line 42 * * column 2, line 1 - line 24 * * column 2, line 47 - column 3, line 37; figures 1,2 * * column 4, line 49 - column 6, line 15; figures 4,5 *		
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The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	27 September 2001	Corsi, F	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
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Application Number  
EP 01 30 5904

DOCUMENTS CONSIDERED TO BE RELEVANT									
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)						
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A	see abstract * column 6, line 5 - line 15; figure 5 * * column 6, line 41 - line 64 *	10,11							
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X,P	WO 01 26087 A (KONINKL PHILIPS ELECTRONICS NV) 12 April 2001 (2001-04-12)  see abstract * page 1, line 9 - page 3, line 14 * * page 8, line 22 - page 9, line 31; figure 5 * * page 12, line 9 - line 16 *	1,2, 12-14, 19-21	TECHNICAL FIELDS SEARCHED (Int.Cl.7)						
<p>The present search report has been drawn up for all claims</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 33%;">Place of search</td> <td style="width: 33%;">Date of completion of the search</td> <td style="width: 33%;">Examiner</td> </tr> <tr> <td>THE HAGUE</td> <td>27 September 2001</td> <td>Corsi, F</td> </tr> </table>				Place of search	Date of completion of the search	Examiner	THE HAGUE	27 September 2001	Corsi, F
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<p>CATEGORY OF CITED DOCUMENTS</p> <table style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;"> X : particularly relevant if taken alone  Y : particularly relevant if combined with another document of the same category  A : technological background  O : non-written disclosure  P : intermediate document </td> <td style="width: 50%;"> T : theory or principle underlying the invention  E : earlier patent document, but published on, or after the filing date  D : document cited in the application  L : document cited for other reasons  &amp; : member of the same patent family, corresponding document </td> </tr> </table>				X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document	T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document				
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